

	\ ZI2		(214
Hardware 210	Name	Valu	le l
P 40Xi (0) 2 0 8 P 40Xe (0) P 40Xe (1) P 40Devices P 40 Devices P 40 Devices P 40 Devices P 40 Device P 40Xi Device 202 204 206	beMulticastCellAgeCounter	1,024	
	bidGrantLatency	8	
	bidScrDisable		
	cellPayloadSize	64	
	chipVersion	1.0	
	clockSpeed	133Mhz	**************************************
	destinationPortFormat	0	
	deviceMode	Ingress	▼
	disableMulticastEnqueue		
	disableUnicastEnqueue	<u> </u>	· · · · · · · · · · · · · · · · · · ·
	freeListSize	0	and the second second
	gbwCtrFreezeOnBackpressure		
	gbwMulticastCellAgeCounter	1,024	
	lid		*
	ignoreQueueBkplnScheduling		
	mayRFMulticastCellAge		

F16.2

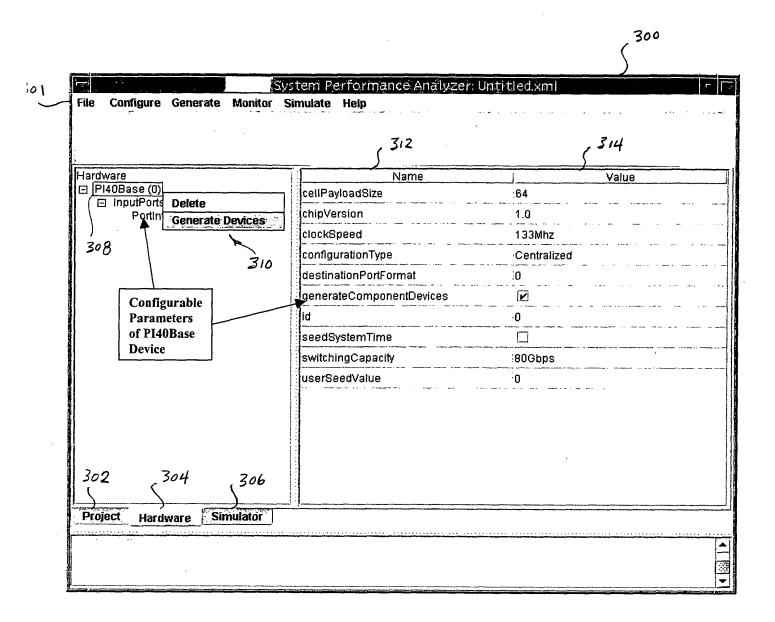


FIG. 3

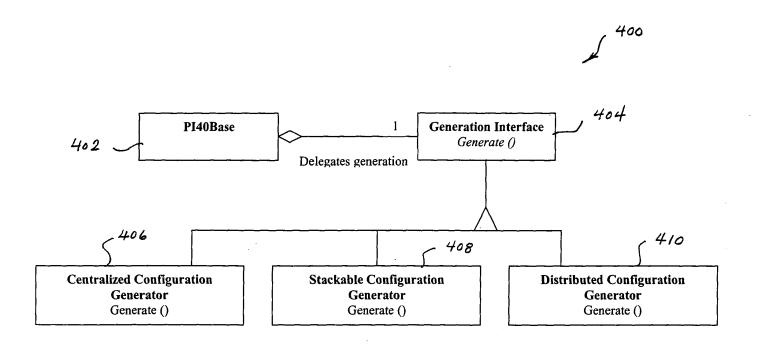


FIG. 4

